



तेजपुरविश्वविद्यालय/ TEZPUR UNIVERSITY

(केंद्रीयविश्वविद्यालय/A Central University)

कुलसचिवकार्यालय/OFFICE OF THE REGISTRAR

तेजपुर-784028 :: असम/ TEZPUR-784028 :: ASSAM

**NOTICE INVITING BID**

**TU/GeM/BoQ-Bid (110)/2021-22/1865** Date 20/9/2022

Bids in 02 Bid System (Technical and Financial) are invited on GeM from reputed manufacturers/authorized dealers/ Service Providers for Supply of Equipment for CPRI RSoP Project, Department of Electrical Engineering, Tezpur University.

Please read the Bid document carefully before participating. It shall be deemed that submission of bid by the bidder has been done after their careful study and examination of the Bid terms with full understanding to its implications. Any lack of information shall not in any way relieve the bidder of its responsibility to fulfill its obligations under the Bid.

The University will reject a proposal for award if it determines that the bidder recommended for award has engaged in corrupt or fraudulent practices in competing for, or in executing, the Purchase Order. Fraudulent Practice means a misrepresentation of facts in order to influence a procurement process and includes collusive practice among bidders (prior to or after Bid submission) designed to establish Bid prices at artificial non-competitive levels and to deprive Tezpur University of the benefits of free and fair competition. Corrupt Practice means offering, giving, receiving or soliciting of anything of value, pressurizing to influence the action of a public official in the process of this purchase execution.

For any queries/doubt please contact the Stores & Purchase Section (e-mail: [snp@tezu.ernet.in](mailto:snp@tezu.ernet.in)) or in the email id mentioned in the GeM Bid.

**GENERAL TERMS & CONDITIONS:**

1. Bidders must visit and inspect the site before submitting their Bid and must upload a certificate obtained from the user department duly signed and stamped as a proof of their visit/inspection of the site along with their bid documents. (Format as per Annexure-II).
2. **Quotations should be accompanied by i) An EMD (in the form of Demand Draft/Banker Cheque) for ₹ 18,000.00 (Rupees Eighteen Thousand) only drawn in favor of Registrar, Tezpur University, payable at Tezpur. No request for consideration of earlier deposited EMD will be considered. ii) Bidders who are MSME/NSIC registered needs to furnish a "Bid Security Declaration" (format enclosed at Annexure – I) in lieu of EMD accepting that if they withdraw or modify their bids during period of validity etc., they will be suspended for a period of Three (03) years from participating in any future bid invited/published by Tezpur University. Bidders who are MSME/NSIC registered seeking exemption from payment of EMD are to submit valid documents in support of their claim. Bidders seeking exemption are asked to clearly mention the category under which exemption is claimed. The category of exemption under MSME/NSIC will be strictly adhered to. iii) The original copies of the EMD/Bid Security Declaration should be deposited/submitted/reach the office of the undersigned before opening of the Bids in an envelope superscribed as "Quotation/Participation Fee and EMD for TU/GEM/BOQ-BID/(110)/2021-22/.....DT-..... addressed to the Assistant Registrar-GA, Tezpur University. Failure to do so may result in rejection of the bid.**
3. Bidders/Tenderers are to ensure that they are GST compliant and that their quoted tax structure/rates are as per GST law. The rates should be exclusive of taxes and applicable tax percentage should be clearly indicated. Financial/Price Bid format where taxes cannot be mentioned separately, in such cases, the percentage of taxes applicable should be mentioned in the Technical Bid document.

*Patel*  
AR(GA)/L



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4. Irresponsive/ incomplete quote will be rejected.
5. No Advance payment will be made.
6. Payment will be made on successful completion of the job and a on submission of satisfactory job completion certificate from concerned authority/officials.
7. **Validity of Quotation:** Quoted rates must be valid for at least **180 days** from the last date of submission of quotation.
8. **Conditional tenders not acceptable:** All the terms and conditions mentioned herein must be strictly adhered to by all the vendors. Conditional tenders shall not be accepted on any ground and shall be rejected straightway. Printed conditions mentioned in the tender bids submitted by vendors will not be binding on Tezpur University.
9. **Enquiry during the course of evaluation not allowed:** No enquiry shall be made by the bidder(s) during the course of evaluation of the tender till final decision is conveyed to the successful bidder(s). However, the Purchase Committee or its authorized representative (Tezpur University) can make any enquiry/seek clarification from the bidders. In such a situation, the agency shall extend full co-operation. The bidders may also be asked to arrange demonstration of the offered items, in a short period notice, as such the bidders have to be ready for the same.

**Termination for default:** Default is said to have occurred.

- a. If the item or any of its component is found having defective parts, poor workmanship, and bad quality of materials used.
- b. If the supplier fails to deliver any or all of the services within the time period(s) specified in the purchase order or any extension thereof granted by Tezpur University.
- c. If the supplier fails to perform any other obligation(s) under the contract.
- d. Under the above circumstances Tezpur University may terminate the contract / purchase order in whole or in part and forfeit the EMD/PBG as applicable. In addition to above, Tezpur University may at its discretion also take the following actions: Tezpur University may procure, upon such terms and in such manner, as it deems appropriate, goods similar to the undelivered items/products and the defaulting supplier shall be liable to compensate Tezpur University for any extra expenditure involved towards goods and services obtained. Besides, the Vice-Chancellor, Tezpur University, and reserves the right to impose any other form of penalty as deemed fit including blacklisting of the vendor.

**10. Selection criteria:**

**i. Evaluation Criteria:**

To ensure that each bidder has the necessary qualifications and resources to full fill its obligations under the contract, the following criteria shall be evaluated. The Eligibility, Financial, Experience & Support criteria should be passed before the technical criteria. Technical criteria will be evaluated and ranked by the award of ACCEPT and/or REJECT. Only the ACCEPTED vendors will be considered for price bid. In case a joint venture makes a bid, any one of the members of the joint venture need to be qualified for each of the criteria mentioned below.

**ii. Eligibility Criteria:**

- a. The bidder or the OEM should not be blacklisted by Tezpur University or any other Educational /R&D/ PSU/ Govt organizations. A certificate or undertaking to this effect must be submitted.

**iii. Technical Criteria**

Technical bids will be evaluated and ranked by the award Accepted & Rejected. The price bids of Accepted vendors would then be considered by the concerned Purchase Committee of the institute.

**iv. Competence Criteria**

For the compatibility and testing competence, a single supplier should bid for all the items and should be capable to supply all the items together.

*Pratibha*  
A.R. (GAR) / K



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**Delivery Period**

11. The job must be completed within **30 Days** of the award of BID without any deviation.

A handwritten signature in blue ink, appearing to read 'Pratik'.

Assistant Registrar-GA,  
Tezpur University



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**Technical Specifications cum Compliance Report**  
**(To be submitted on Company's/Firm's Letterhead Signed and Sealed)**

Item:

SL No	Parameter	Description
1	LCR meter (100 kHz)	<b>Technical Specification:</b> <ul style="list-style-type: none"><li>• L: 20mH---2000H</li><li>• C: 200pf---20mF</li><li>• R: 20Ω—200MΩ</li><li>• Basic precision: 0.5%</li><li>• Output impedance: 50 Ω-120Ω</li><li>• DCR: DC resistance measurement mode</li><li>• D: Loss factor</li><li>• Q: Quality factor</li><li>• <math>\theta</math>: Phase location angle</li><li>• ESR : Equivalent serial resistance</li><li>• EPR: Equivalent parallel resistance</li><li>• DUT: Articles to measure</li><li>• USB communication function</li><li>• PC Communication Protocol</li><li>• Primary display digit of 19,999 and Secondary display digit 1,999</li><li>• Relative measurement &amp; sieving function</li><li>• Ambient Conditions<ol style="list-style-type: none"><li>1)Altitude:&lt;2000 meters</li><li>2)Storage humidity: =75%RH</li><li>3)Operating environment: 0°C~ 40°C</li><li>4)Storage environment: -20°C ~ +50°C</li></ol></li></ul>
2	Variable Inductor	<b>Technical Specification:</b> <ul style="list-style-type: none"><li>• 100uH-to-1 m H, at 100kHz</li><li>• Current 35A</li><li>• ESR: &lt; 5mOhm</li></ul>
3	Digital Storage Oscilloscope	<b>Technical Specification:</b> <ul style="list-style-type: none"><li>• 8-bit, 2+1 channel, 200 MHz, 1 GSPS, 40 Mpts</li><li>• Analog Channel : 2+1 Channel</li><li>• 2 Channel AWG (25 MHz)</li><li>• Bandwidth: 200MHz</li><li>• Integrated at least 48 Wh Li-Ion battery backup for isolated measurement</li><li>• <math>\frac{3}{4}</math> digit Multimeter basic Multimeter measurement</li><li>• Max. Input Voltage: 1 MΩ ≤ 300 Vrms</li><li>• Probe Attenuation Factor: 0.001X – 1000X</li><li>• Bus Decoder: 12C, SPI, RS232, and CAN</li><li>• Sample Accuracy: ±1ppm</li><li>• Record Length: 40M</li><li>• Waveform Refresh Rate: 75,000 wfms/s</li><li>• Display: 8" Colour LCD, 800 X 600 pixels</li></ul>

*Prasanna*  
19/11/22



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		<ul style="list-style-type: none"><li>• Probe Attenuation Factor: 0.001X -1000X</li><li>• Trigger Type: Edge, Video, Pulse, Slope, Runt, Windows, Time Out, Nth Edge, Logic, 12C, SPI, RS232, and CAN</li><li>• Communication Interface: USB host, USB device, USB port for Pict Bridge, Trig Out (P/F), LAN</li></ul>
4	Digital Power Analyser/Logger	<p>Technical Specification:</p> <ul style="list-style-type: none"><li>• Three Phase AC/DC Benchtop Digital Power Analyser/Logger</li><li>• Measurement Bandwidth upto 100kHz</li><li>• Measurement Type: DC, RMS, Mean</li><li>• Fundamental measurement up to 50<sup>th</sup> order</li><li>• Optional current measurement up to 200A</li><li>• 100mS logging Interval</li><li>• D/A conversion resolution: 16 bits</li><li>• DC, Single-phase, three-wire (1P3W); three-phase, three-wire (3P3W); three-phase four-wire (3P4W); or three-voltage, three-current (3V3A).</li><li>• Output items: U,I, P, S,Q ,<math>\phi</math>, fU, <math>\lambda</math> fi, Upk, Ipk, WP<math>\pm</math>, q, q<math>\pm</math> and MATH<ul style="list-style-type: none"><li>○ 0.1Hz to 100 kHz, 20 A, 600V</li></ul></li><li>• Communication: USB, Ethernet, GPIB IEEE488.2, D-Sub 9-pin (plug)</li><li>• ATE ready</li><li>• At least 24 Hours of constant data recoding for all possible parameter</li><li>• Data-logging &amp; Waveform display data in. csv format</li><li>• Graphical measurement view</li><li>• Lab VIEW supported driver</li><li>• PC Communication software</li></ul>
5	DSP Board, Programmer and Software	<p>Technical Specification:</p> <ul style="list-style-type: none"><li>• 32bit Dual Core DSP Microcontroller</li><li>• Precision Floating-Point Architecture with at-lest 800 MIPS</li><li>• Integrated CLA</li><li>• At least 1 MB Flash Memory</li><li>• 16 Bit ADC</li><li>• 16 High-Resolution PWM</li><li>• 8 Channel Sigma Delta Demodulator</li><li>• Integrated CLB</li><li>• Microcontroller Experiment set-up for all Peripherals, USB, CAN and Isolated Debugging Interface with at least 3kV voltage Isolation</li><li>• Date-Storage facility in SD Card</li><li>• Integrated Development Environment</li><li>• More than 6 DMA Channel</li><li>• More than 160 GPIO channel</li></ul>

*Prabha*  
19/11/22



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Annexure-I

*(To be submitted on Company's/Firm's Letterhead signed and sealed)*

**Bid-Security Declaration Form**

Date: \_\_\_\_\_

Bid No. \_\_\_\_\_

To (insert complete name and address of the purchaser)

I/We. The undersigned, declare that:

I/We understand that, according to your conditions, bids must be supported by a Bid Security Declaration.

I/We accept that I/We may be disqualified from bidding for any contract with you for a period of three year from the date of notification if I am /We are in a breach of any obligation under the bid conditions, because I/We

(a) have withdrawn/modified/amended, impairs or derogates from the tender, my/our Bid during the period of bid validity specified in the form of Bid; or

(b) having been notified of the acceptance of our Bid by the purchaser during the period of bid validity (i) fail or reuse to execute the contract, if required, or (ii) fail or refuse to furnish the Performance Security, in accordance with the Instructions to Bidders.

I/We understand this Bid Securing Declaration shall cease to be valid if I am/we are not the successful Bidder, upon the earlier of (i) the receipt of your notification of the name of the successful Bidder; or (ii) thirty days after the expiration of the validity of my/our Bid.

Signed: (insert signature of person whose name and capacity are shown) in the capacity of (insert legal capacity of person signing the Bid Securing Declaration).

Name: (insert complete name of person signing he Bid Securing Declaration)

Duly authorized to sign the bid for an on behalf of: (insert complete name of Bidder) Dated

on \_\_\_\_\_ day of \_\_\_\_\_ (insert date of signing)

Corporate Seal (where appropriate)

(Note: In case of a Joint Venture, the Bid Securing Declaration must be in the name of all partners to the Joint Venture that submits the bid).